

## APPLICATION DATA SHEET

### APPLICATION INFORMATION

Application Type::	REGULAR
Subject Matter::	UTILITY
CD-ROM or CD-R?::	NONE
Title::	PARASITIC CAPACITANCE EXTRACTING DEVICE AND METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT
Attorney Docket Number::	239479US2
Total Drawing Sheets::	11
Small Entity?::	NO

### INVENTOR INFORMATION

Applicant Authority Type::	INVENTOR
Primary Citizenship Country::	JAPAN
Status::	FULL CAPACITY
Given Name::	Toshiki
Family Name::	KANAMOTO
City of Residence::	TOKYO
Country of Residence::	JAPAN
Street of Mailing Address::	c/o Renesas Technology Corp., 4-1, Marunouchi 2-chome, Chiyoda-ku,
City of Mailing Address::	TOKYO
Country of Mailing Address::	JAPAN
Applicant Authority Type::	INVENTOR
Primary Citizenship Country::	JAPAN
Status::	FULL CAPACITY
Given Name::	Hirokazu
Family Name::	IKEDA
City of Residence::	TOKYO
Country of Residence::	JAPAN
Street of Mailing Address::	c/o Renesas Technology Corp., 4-1, Marunouchi 2-chome, Chiyoda-ku,
City of Mailing Address::	TOKYO
Country of Mailing Address::	JAPAN

#### CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

#### REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

#### FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2003-030376	Japan	02/07/03	YES

#### ASSIGNMENT INFORMATION

Assignee Name:: Renesas Technology Corp.  
Street of Mailing Address:: 4-1, Marunouchi 2-chome,  
Chiyoda-ku,  
City of Mailing Address:: TOKYO  
Country of Mailing Address:: JAPAN